



DECCAN EDUCATION SOCIETY - Since 1884

DES PUNE UNIVERSITY

DESPU/VC/220101/2025-2026/111

School of Engineering and Technology

Second Year B.Tech Electronics and Communication Engineering (AIML)

(Semester – III)

BACKLOG (Candidates Admitted in the Academic Year 2024-2025)

AND

REGULAR (Candidates Admitted in the Academic Year 2025-2026)

SCHEDULE OF END SEMESTER EXAMINATION (THEORY), DECEMBER 2025

INSTRUCTIONS FOR CANDIDATES

1. Candidates are required to be present in the examination hall **fifteen minutes before** the scheduled time of examination.
2. Candidates are **not allowed** to enter the examination hall **after fifteen minutes**, the scheduled time of examination.
3. Candidates must carry their college Identity Card and Hall Ticket at the time of examination.
4. Candidates are not allowed to take any material into the examination hall that can be used for malpractice during the examination.
5. Candidates must check the DES Pune University website **www.despu.edu.in** regularly for the changes if any, which may be notified later in the examination schedule.
6. Candidates are not allowed to carry any electronics gadgets like mobile phones, wireless Bluetooth based gadgets like ear plugs, headphones, smart watch etc. in the examination hall that can be used for malpractice during the examination.
7. Candidates must bring their own instruments line side rulers, eraser, sharpener, pen, pencil etc. and will not be allowed to borrow from each other during examination.
8. Candidates are **not allowed** to carry the answersheet outside the examination hall.
9. No request for any special concession such as a change in time or any day fixed for the examination on any ground shall be granted.
10. The written examination will be conducted as per the following timetable.

Second Year B.Tech Electronics and Communication Engineering (AIML)

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BACKLOG

(Candidates Admitted in the Academic Year 2024-2025)

Day and Date	Course Code	Course Name	Time
Monday 17/11/2025	HEE0921324	Engineering Economics	10:30 AM To 11:30 AM
Tuesday 18/11/2025	EEE0320524	Digital Electronics	10:30 AM To 11:30 AM
Wednesday 19/11/2025	EEE0520724	Data Structures and Algorithms	10:30 AM To 11:30 AM
Thursday 20/11/2025	EEE0320124	Electronic Devices and Circuits	10:30 AM To 11:30 AM
Friday 21/11/2025	EEE0320324	Signals and Systems	10:30 AM To 12:30 PM

NOTE:

1. Students should contact School Examination Officer for the Examination Schedule which is to be conducted at School level - Continuous Assessment (CA) of Theory courses, Continuous Assessment (CA) and End Semester Examination (ESE) of Practical courses and End Semester Examination of Theory Courses (if any) **not mentioned above.**
2. Students should contact School Examination Officer for the **Practical End Semester Examination (if Any)** Schedule which is to be conducted at School level.

Date: 15/10/2025




Controller of Examinations (I/C)
DES Pune University, Pune

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REGULAR

(Candidates Admitted in the Academic Year 2025-2026)

Day and Date	Course Code	Course Name	Time
Tuesday 18/11/2025	ETEC202	Digital Electronics	10:30 AM To 12:30 PM
Wednesday 19/11/2025	ETCS300	Data Structures I	10:30 AM To 12:30 PM
Thursday 20/11/2025	ETEC200	Electronic Devices and Circuits	10:30 AM To 12:30 PM
Friday 21/11/2025	ETEC201	Signals and Systems	10:30 AM To 12:30 PM
Monday 24/11/2025	HSPP111	Framework of Indian Democracy	10:30 AM To 11:30 AM
	OR		
	HSL120	संस्कृतभाषापरिचय:- 1	
	OR		
	HSL121	German for Beginners I	
	OR		
	HSL122	Elementary French I	
	OR		
	BMFI206	Fundamentals of Accounting and Finance	

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