



DECCAN EDUCATION SOCIETY - Since 1884

DES PUNE UNIVERSITY

DESPU/VC/220101/2025-2026/304

School of Engineering and Technology

Second Year B. Tech Electronics and Communication Engineering (AIML) (Semester – IV)

BACKLOG (Candidates Admitted in the Academic Year 2024-2025)

and

REGULAR (Candidates Admitted in the Academic Year 2025-2026)

SCHEDULE OF END SEMESTER EXAMINATION (THEORY), APRIL 2026

INSTRUCTIONS FOR CANDIDATES

1. Candidates are required to be present in the examination hall **fifteen minutes before** the scheduled time of examination.
2. Candidates are **not allowed** to enter the examination hall **after fifteen minutes**, the scheduled time of examination.
3. Candidates must carry their college Identity Card and Hall Ticket at the time of examination.
4. Candidates are not allowed to take any material into the examination hall that can be used for malpractice during the examination.
5. Candidates must check the DES Pune University website **www.despu.edu.in** regularly for the changes if any, which may be notified later in the examination schedule.
6. Candidates are not allowed to carry any electronics gadgets like mobile phones, wireless Bluetooth based gadgets like ear plugs, headphones, smart watch etc. in the examination hall that can be used for malpractice during the examination.
7. Candidates must bring their own instruments line side rulers, eraser, sharpener, pen, pencil etc. and will not be allowed to borrow from each other during examination.
8. Candidates are **not allowed** to carry the answersheet outside the examination hall.
9. No request for any special concession such as a change in time or any day fixed for the examination on any ground shall be granted.
10. The written examination will be conducted as per the following timetable.

Second Year B. Tech Electronics and Communication Engineering (AIML)
(Semester – IV)

BACKLOG

(Candidates Admitted in the Academic Year 2024-2025)

Day and Date	Course Code	Course Name	Time
Wednesday 13/05/2026	EEE0320224	Integrated Circuits and Applications	02.30 PM – 03.30 PM
Thursday 14/05/2026	EEE0320424	Communication Systems	02.30 PM – 03.30 PM
Friday 15/05/2026	EEE0320624	Microcontrollers and Embedded Systems	02.30 PM – 03.30 PM

NOTE:

1. Students should contact School Examination Officer for the Examination Schedule which is to be conducted at School level - Continuous Assessment (CA) of Theory courses, Continuous Assessment (CA) and End Semester Examination (ESE) of Practical courses and End Semester Examination of Theory Courses (if any) **not mentioned above**.
2. Students should contact School Examination Officer for the **Practical End Semester Examination (if Any)** Schedule which is to be conducted at School level.

Date: 27/03/2026




Controller of Examinations (I/C)
DES Pune University, Pune

Second Year B. Tech Electronics and Communication Engineering (AIML)
(Semester – IV)

REGULAR

(Candidates Admitted in the Academic Year 2025-2026)

Day and Date	Course Code	Course Name	Time
Tuesday 12/05/2026	HSP161	Contours of Indian Statecraft: Past to Present	02:30 PM - 03:30 PM
	OR		
	HSL170	संस्कृतभाषापरिचय:- 2	
	OR		
	HSL171	German for Beginners II	
	OR		
	HSL172	Elementary French II	
	OR		
	BMFI209	Corporate Finance and Financial Markets	
Wednesday 13/05/2026	ETEC203	Integrated Circuits and Applications	02.30 PM – 04.30 PM
Thursday 14/05/2026	ETEC204	Communication Systems	02.30 PM – 04.30 PM
Friday 15/05/2026	ETEC205	Microcontrollers and Embedded Systems	02.30 PM – 04.30 PM

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